ECMT1V20 Common mode choke, through-hole



Product features

- Closed magnetic path reduces conductive EMI emission
- High impedance and inductance values
- Robust construction
- · High voltage isolation
- Independent winding sections
- Rated voltage: 250 Vac

Applications

- Industrial IoT equipment
- Motion controls
- Power supplies
- Battery backup
- Renewable energy products
- Smart meters
- Solar/wind generators, inverters, charger controllers
- · Medical equipment
- High tech consumer products
- Appliances

Environmental compliance and general specifications

- Storage temperature range (Component): -40 °C to +85 °C
- Operating temperature range: -40 °C to +125 °C (ambient plus self-temperature rise)
- Wave solder temperature: +260 °C maximum







Product specifications

Part number ⁷	OCL ¹ (mH) minimum (1-2), (4-3)	DCR ² (Ω) maximum (1-2), (4-3) @ +25 °C	I _{rms} ³ (A) (1-4) short 2,3	SRF (kHz) minimum	Hi-pot⁴ (Vac)	Hi-pot⁵ (Vac)	Insulation resistance ⁶ (MΩ) minimum	
ECMT1V2023S-2R0-R	2.0	0.08	1.5	976	1500	1000	100	-
ECMT1V2017H-2R0-R	2.0	0.08	1.5	976	1500	1000	100	_
ECMT1V2023S-200-R	20	0.55	1.0	245	1500	1000	100	-
ECMT1V2017H-200-R	20	0.55	1.0	245	1500	1000	100	-
ECMT1V2023S-300-R	30	0.9	0.8	160	1500	1000	100	-
ECMT1V2017H-300-R	30	0.9	0.8	160	1500	1000	100	
ECMT1V2023S-600-R	60	2.1	0.4	96	1500	1000	100	
ECMT1V2017H-600-R	60	2.1	0.4	96	1500	1000	100	-
AC currents. PCB layout, trac	e method measured from the r an approximate temperatu e thickness and width, air-fl t is recommended that the t		ting components will	5. Hi-pot: Coil-Co 6. Insulation Resi	efinition: ECMT1Vxx duct code	l Coil-Core, at 500 Vdc xxy-zzz-R		C

3. Ims: Maximum DC current for an approximate temperature rise of 40 °C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed +125 °C under worst case operating conditions verified in the end application.

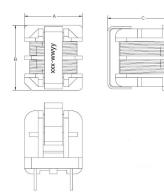
xxxx= Size indicator

y= Orientation H= horizontal, S= vertical

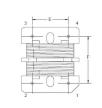
zzz=Inductance value in mH, R= decimal point, If no R is present last digit indicates number of zeros -R= RoHS compliant

Mechanical parameters, schematic, pad layout (mm)

ECMT1V2023S-xxx-R

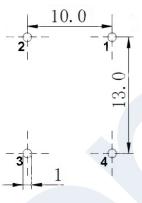


ECMT1V2017H-xxx-R

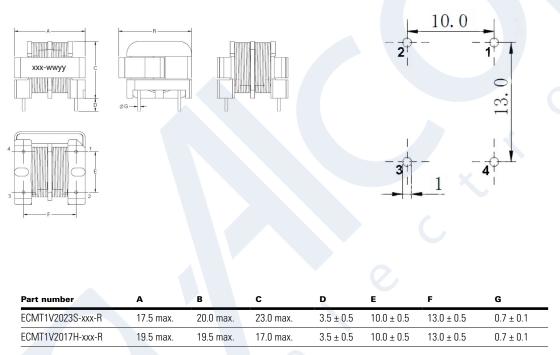


Recommended PCB layout





Recommended PCB layout



Schematic

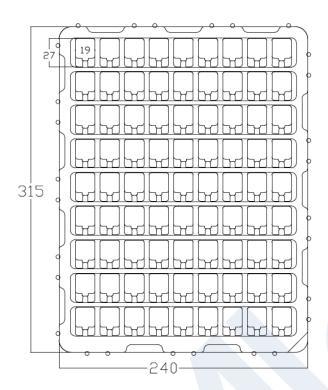


Part marking: xxx-wwyy, xxx =inductance value in mH, wwyy= lot code Traces or vias underneath the inductor is not recommended

Technical Data **ELX1093** Effective September 2021

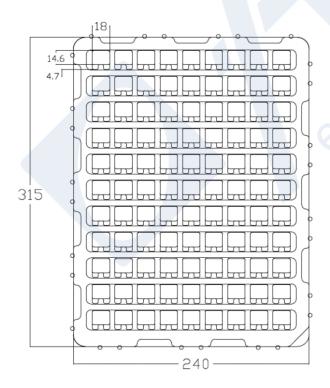
Packaging information (mm)

ECMT1V2023S-xxx-R Supplied in tray, 10 trays per carton. (81 parts per tray x 10 trays per box = 810 parts per carton) (Tray height 22 mm)



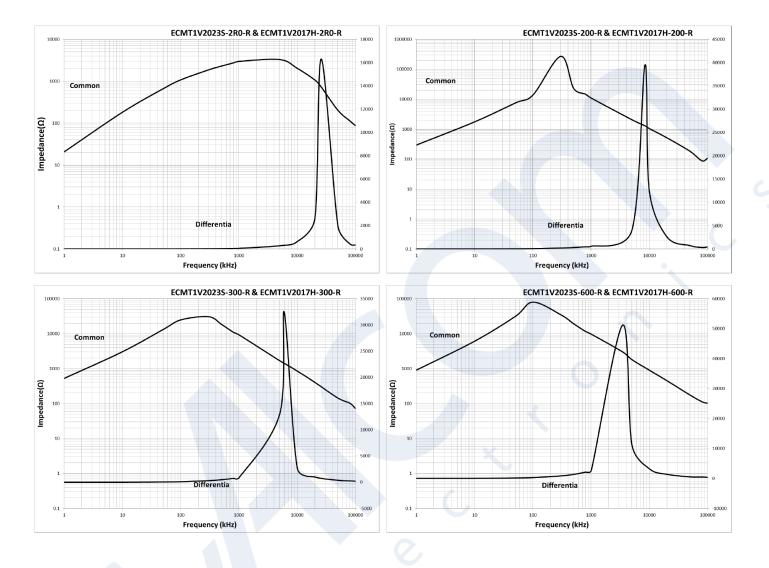
ECMT1V2017H-xxx-R

Supplied in tray, 10 trays per carton. (99 parts per tray x 10 trays per box = 990 parts per carton) (Tray height 24 mm)

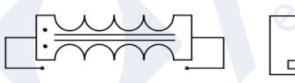


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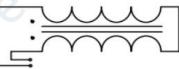
Impedance vs frequency



Measurement method

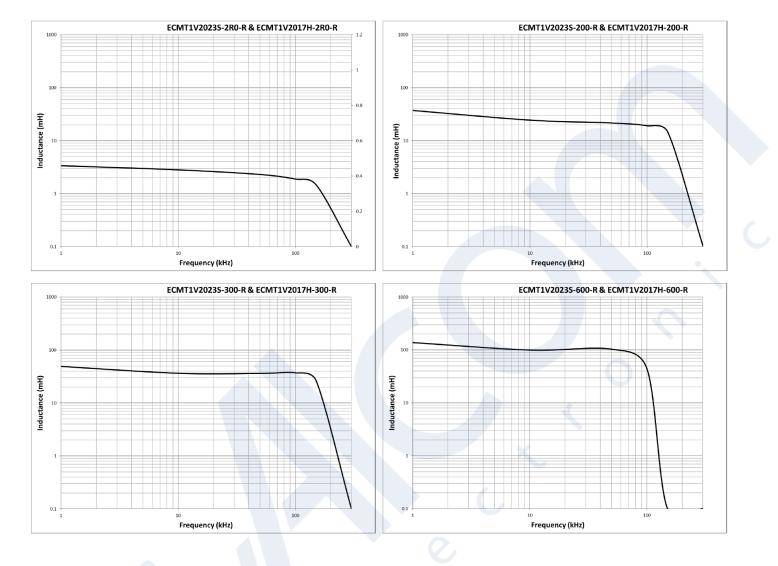


Common Mode



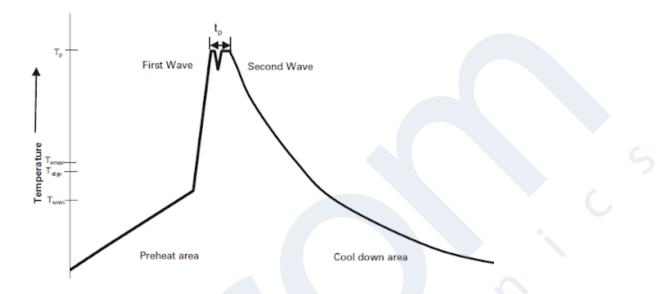
Differential Mode

Inductance vs frequency



ECMT1V20 Common mode choke, through-hole

Wave solder profile



Reference EN 61760-1:2006

Profile feat	ure	Standard SnPb solder	Lead (Pb) free solder	
Preheat	• Temperature min. (T _{smin})	100 °C	100 °C	
	• Temperature typ. (T _{Styp})	120 °C	120 °C	
	• Temperature max. (T _{smax})	130 °C	130 °C	
	• Time (T _{smin} to T _{smax}) (t _s)	70 seconds	70 seconds	
$\overline{\Delta}$ preheat to max Temperature		150 °C max.	150 °C max.	
Peak temperature (Tp)*		235 °C – 260 °C	250 °C – 260 °C	
Time at peak temperature (t _p)		10 seconds max 5 seconds max each wave	10 seconds max 5 seconds max each wave	
Ramp-down rate		~ 2 K/s min ~3.5 K/s typ ~5 K/s max	~ 2 K/s min ~3.5 K/s typ ~5 K/s max	
Time 25 °C to 25 °C		4 minutes	4 minutes	

Manual solder

+350 °C, 4-5 seconds. (by soldering iron), generally manual, hand soldering is not recommended.

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