



For Automotive, Built-in 32.768 kHz DTCXO, High Stability, +125 °C

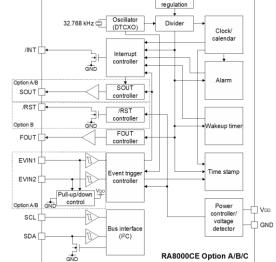
RA8000CE

- Built in frequency adjusted 32.768 kHz crystal unit and DTCXO
- Interface Type
- Time stamp function
- : 2 times stamped from year to second · Reset functions with a delay

: I²C-Bus

- : Detect a main power supply and remove the reset Wake up every minute or every second
- Interrupt output Alarm interruption
- : Day, date, hour, minute, second • Auto repeat wakeup timer interruption
- Self-monitoring interruption : Crystal oscillation stop, VDD low
- AEC-Q100 compliant

Block diagram Theoretica



Pin Function

Signal Name	I/O	Function
EVIN1, 2	Input	External event input pins Pull-up and pull-down is configurable by the resisters
SCL	Input	Serial clock input pin
SDA	Input / Output	Serial data input and output pin
FOUT	Output	Frequency output pin (CMOS). 32.768 kHz (default), 1024 Hz or 1 Hz clock output is selectable. This pin can be switched to the wakeup timer interrupt output (CMOS)
/INT	Output	Interrupt output pin (N-ch. open drain). The wakeup timer, time update, alarm, and/or event detection interrupt signals can be selected to output from this pin. When two or more signals are selected, they are NORed before being output.
/RST	Output	Reset output pin (N-ch. open drain)
SOUT	Output	Status output pin
Vdd	-	Power-supply pin
GND	-	Ground pin

Specifications (characteristics)

 Electrical Characteristics 									
Item	Symbol	Symbol Conditi		ions		Min.	Тур.	Max.	Unit
Operating voltage	VDD	-				1.6	3.0	5.5	V
Temp. compensated Voltage	VTEM	-				1.6	3.0	5.5	V
Clock supply voltage	VCLK	-				1.3	3.0	5.5	V
Operating temperature	Ta	-			-40	+25	+125	°C	
	∆f/f	YB	T _a = -40 °C to +85 °C		±5.0			x 10 ⁻⁶	
Frequency tolerance			Ta = +85 °C to +105 °C		±8.0				
			Ta = +105 °C to +125 °C			±50.0			
Current consumption	IDD1	/INT = Hi-Z, FOUT:		No	$V_{DD} = 5 V$	-	0.35	1.8	
	DD2		OFF (Hi-Z),	/RST pin	Vdd = 3 V	-	0.3	1.7	
	IDD11	Temp. Compensation interval 2.0 s, SCL = SDA = H		(DOT allo	$V_{DD} = 5 V$	-	1.5	3.7	_
	IDD12				$V_{DD} = 2 V$	-	0.6	2.25	



Product Number (2,000 pcs / Reel) RA8000CE YB A0 : X1B000501A00115 RA8000CE YB B8 : X1B000501A00915 RA8000CE YB C0 : X1B000501A01015



RA8000CE (3.2 × 2.5 mm, t = 1.0 mm Max.)

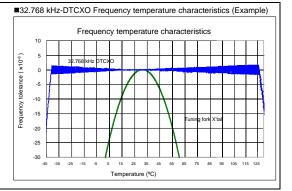
Overview

- Interface type
- I²C-Bus interface Fast-Mode 400 kHz
- High stability
 - YB: ±5.0 x 10⁻⁶ / -40 °C to +85 °C (Monthly rate: ±13.2 seconds) $\pm 8.0 \times 10^{-6} / +85 \circ$ C to +105 °C (Monthly rate: ±21 seconds) ±50.0 × 10⁻⁶ / +105 °C to +125 °C (Monthly rate: ±132 seconds)
 - Clock output function
 - Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz Wakeup timer function
 - Can generate an interrupt in 976.56 µs to 32-year cycle.
 - Can be used as a time integration meter.
- Can be used as a watchdog timer.
- Time stamp function Record data: 1/1024 seconds to 1 second, seconds, minutes, hours, days, months, years.
- Number of recordable events: 2 events
- Trigger source: External event (EVIN) input, voltage drop/oscillation stop status detected, command input from the host.
- EVIN pin has function of chattering-cancel.
- Reset function with a delay Can output a reset signal when a VDD voltage drop status is detected. Status output (SOUT)
- Can output the selected internal flag (interrupt flag, voltage drop detection flag) status.

Terminal connection / External dimensions (Unit: mm) RA8000CE 1. EVIN1 SOUT 10. Vdd GND 2. 9. 3 /INT 8 SCL FOUT SDA 4. 7. 2.5 ± 0.2 /RST 6. EVIN2 5.

*The above diagram is the terminal layout for Option B. For other options, please refer to the Pin Option section.

* Refer to application manual for details



SEIKO EPSON CORPORATION



Pin Option

		Dia Ma		Pin name			
		Pin No.	Option A	Option B	Option C		
		1	EV		N.C.		
		2	Vdd				
		3	/INT				
		4		FOUT			
		5	N.C.	/RST	N.C.		
		6	EV	IN2	N.C.		
		7		SDA			
		8		SCL			
		9		GND			
		10	SO	UT	N.C.]	
Pin name EVIN1 V _{DD} /INT FOUT N.C.	1 2 3 4	No. Pin nai 10 SOUT 9 GND 8 SCL 7 SDA 6 EVIN2	г	F	Pin name No. EVIN1 1 V _{DD} 2 /INT 3 FOUT 4 /RST 5	10 9 8 8 7	Pin name SOUT GND SCL SDA EVIN2
Pin name N.C. V _{DD} /INT FOUT N.C.	1 2 3 4	No. Pin nar 10 N.C. 9 GND 8 SCL 7 SDA 6 N.C.	me				

Product name

RA8000CE	YΒ	<u>A 0</u>
1	2	34

- ① Model CE type package 3.2 x 2.5 x 1.0 mm
- ② Frequency tolerance
 - YB: ±5.0 x 10⁻⁶ / -40 °C to +85 °C (Monthly rate: ±13.2 seconds) ±8.0 x 10⁻⁶ / +85 °C to +105 °C (Monthly rate: ±21 seconds) ±50.0 x 10⁻⁶ / +105 °C to +125 °C (Monthly rate: ±132 seconds)
- 3 Pin Option
 - A: Option A
 - B: Option B
 - C: Option C
- ④ Reset output function
 - 0: No /RST pin
 - 8: With /RST pin (VDD drop detection voltage: +2.4 V Typ.)

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